

IN THE CLAIMS:

1. (Currently Amended) A method, comprising:
identifying at least one wafer to be processed;
identifying a process tool in which said at least one wafer is to be processed;
obtaining enhanced metrology data regarding a process operation to be performed in said
identified process tool prior to processing said identified at least one wafer in said
identified process tool; ~~and~~
analyzing at least said enhanced metrology data to determine if said process tool is
acceptable for processing said identified at least one wafer; and
if said process tool is determined to be acceptable, positioning said at least one wafer in
said identified process tool and performing said process operation thereon.
2. (Canceled)
3. (Original) The method of claim 1, wherein said identified at least one wafer is
part of a lot of wafers identified for processing in said identified process tool.
4. (Original) The method of claim 1, wherein said identified at least one wafer is a
wafer wherein production integrated circuit devices are formed thereon.
5. (Original) The method of claim 1, wherein said identified at least one wafer is a
test wafer.

6. (Original) The method of claim 1, wherein said identified process tool is at least one of a deposition tool, an etch tool, a furnace, an ion implant tool, a chemical mechanical polishing tool and a photolithography tool.

7. (Original) The method of claim 1, wherein said step of obtaining enhanced metrology data regarding said process operation comprises obtaining a greater amount of metrology data for said process operation relative to an amount of metrology data obtained for said process operation in accordance with a previously established metrology sampling plan.

8. (Original) The method of claim 1, wherein said step of obtaining enhanced metrology data regarding said process operation comprises obtaining a different type of metrology data for said process operation relative to a type of metrology data obtained for said process operation in accordance with a previously established metrology sampling plan.

9. (Original) The method of claim 1, wherein said step of obtaining enhanced metrology data regarding said process operation comprises increasing a frequency at which metrology data for said process operation is acquired relative to a frequency at which metrology data for said process operation is acquired in accordance with a previously established metrology sampling plan.

10. (Currently Amended) A method, comprising:
identifying at least one lot of wafers to be processed;
identifying a process tool in which said at least one lot of wafers is to be processed;

obtaining enhanced metrology data regarding a process operation to be performed in said identified process tool prior to processing said identified at least one lot of wafers in said identified process tool; ~~and~~
analyzing at least said enhanced metrology data to determine if said process tool is acceptable for processing said identified at least one wafer; and
if said process tool is determined to be acceptable, positioning said at least one wafer from said identified lot of wafers in said identified process tool and performing said process operation thereon.

11. (Canceled)

12. (Original) The method of claim 10, wherein said identified at least one lot of wafers is comprised of a plurality of wafers having production integrated circuit devices formed thereon.

13. (Original) The method of claim 10, wherein said identified at least one lot of wafers is comprised of a plurality of test wafers.

14. (Original) The method of claim 10, wherein said identified process tool is at least one of a deposition tool, an etch tool, a furnace, an ion implant tool, a chemical mechanical polishing tool and a photolithography tool.

15. (Original) The method of claim 10, wherein said step of obtaining enhanced metrology data regarding said process operation comprises obtaining a greater amount of

metrology data for said process operation relative to an amount of metrology data obtained for said process operation in accordance with a previously established metrology sampling plan.

16. (Original) The method of claim 10, wherein said step of obtaining enhanced metrology data regarding said process operation comprises obtaining a different type of metrology data for said process operation relative to a type of metrology data obtained for said process operation in accordance with a previously established metrology sampling plan.

17. (Original) The method of claim 10, wherein said step of obtaining enhanced metrology data regarding said process operation comprises increasing a frequency at which metrology data for said process operation is acquired relative to a frequency at which metrology data for said process operation is acquired in accordance with a previously established metrology sampling plan.

18. (Currently Amended) A method, comprising:

identifying at least one lot of wafers to be processed;

identifying a process tool in which said at least one lot of wafers is to be processed;

in response to the identification of said at least one lot of wafers and the identification of said process tool, increasing a frequency at which metrology data regarding a process operation to be performed in said identified process tool is acquired prior to processing said identified at least one lot of wafers in said identified process tool; and

analyzing at least said metrology data acquired as a result of the increased frequency at which metrology data is acquired to determine if said process tool is acceptable for processing said at least one lot of wafers; and
if said process tool is determined to be acceptable, positioning at least one wafer from said identified lot in said identified process tool and performing said process operation thereon.

19. (Canceled)

20. (Original) The method of claim 18, wherein said identified at least one lot of wafers is comprised of a plurality of wafers wherein production integrated circuit devices are formed thereon.

21. (Original) The method of claim 18, wherein said identified at least one lot of wafers is comprised of a plurality of test wafers.

22. (Original) The method of claim 18, wherein said identified process tool is at least one of a deposition tool, an etch tool, a furnace, an ion implant tool, a chemical mechanical polishing tool and a photolithography tool.

23. (Original) The method of claim 18, wherein said step of increasing the frequency at which metrology data is acquired comprises obtaining a greater amount of metrology data for said process operation relative to an amount of metrology data obtained for said process operation in accordance with a previously established metrology sampling plan.

24. (Currently Amended) A method, comprising:

identifying at least one lot of wafers to be processed;

identifying a process tool in which said at least one lot of wafers is to be processed;

in response to the identification of said at least one lot of wafers and the identification of

said process tool, increasing an amount of metrology data acquired regarding a

process operation to be performed in said identified process tool prior to

processing said identified at least one lot of wafers in said identified process tool;

and

analyzing at least said increased amount of metrology data acquired to determine if said

process tool is acceptable for processing said identified at least one wafer; and

if said process tool is determined to be acceptable, positioning said at least one wafer

from said identified lot of wafers in said identified process tool and performing

said process operation thereon.

25. (Canceled)

26. (Original) The method of claim 24, wherein said identified at least one lot of wafers is comprised of a plurality of wafers having production integrated circuit devices formed thereon.

27. (Original) The method of claim 24, wherein said identified at least one lot of wafers is comprised of a plurality of test wafers.

28. (Original) The method of claim 24, wherein said identified process tool is at least one of a deposition tool, an etch tool, a furnace, an ion implant tool, a chemical mechanical polishing tool and a photolithography tool.